Trusted Computing

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Introduction

Complex software systems are (eventually) **flawed**

Design flaws: hard to provide the intended security guarantees

Implementation flaws: even when design is correct, bugs might introduce vulnerabilities

Introduction

Formal models of security

Can we mathematically **prove** security?

Formal models of computer security can be used to "prove" that:

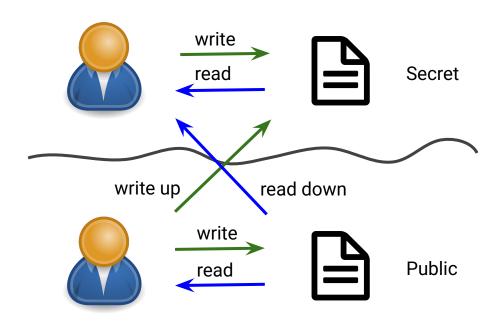
- design satisfies a set of security requirements
- implementation conforms to the design

Example: Bell - La Padula (BLP)

Definition: Information should never flow from a level to lower ones

- Simple security: Subjects cannot read from objects at a higher level
- *-property: Subjects cannot write into objects classified at a lower level

(plus standard DAC)

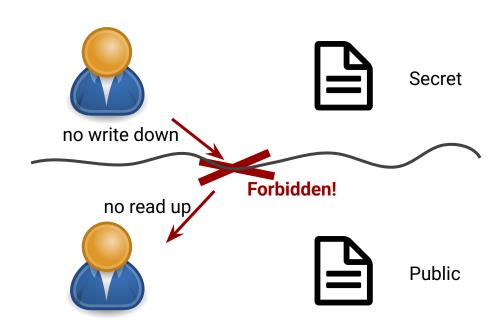


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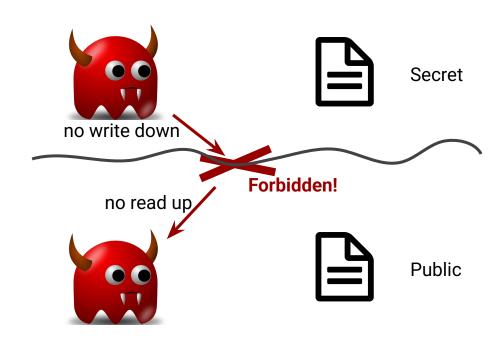


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BLP model

BLP can be stated **formally**

Assume: S_1 , ..., S_m subjects, O_1 , ..., O_n objects, A_1 , ..., A_w access modes (e.g., read, write, append, ...)

State: 3-tuple (b, M, f), defined as

b: **current access set** of triples (S_i, O_j, A_x) representing subject S_i accessing object O_i in mode A_x

M: access matrix of permitted access modes. M_{ij} contains modes for subject S_i accessing object O_i

f: **level function** assigning a security level to subjects and objects

 $\mathbf{f}(\mathbf{O_j})$ is the security level of object $\mathbf{O_j}$

 $f(S_i)$ is the security level of subject S_i

BLP secure state

Simple security: every triple of the form $(S_i, O_j, read)$ in the current access set **b** has the property

$$f(S_i) \ge f(O_j)$$

*-property: every triple of the form (S_i, O_j, write) in the current access set **b** has the property

$$f(S_i) \leq f(O_j)$$

In addition to **MAC**, BLP also enforces **DAC**, in terms of the access control matrix **M**. DAC is formalized as follows:

ds-property: if (S_i, O_j, A_x) is a current access in **b**, then access mode A_x is present in M_{ij} . That is

$$A_{x} \in M_{ij}$$

BLP secure state

In summary, we say that a state (b, M, f) is secure iff

Simple security:
$$\forall i j . (S_i, O_i, read) \in b \Rightarrow f(S_i) \ge f(O_i)$$

*-property:
$$\forall i j . (S_i, O_j, write) \in b \Rightarrow f(S_i) \le f(O_j)$$

ds-property:
$$\forall i j x . (S_{i'} O_{j'} A_x) \in b \Rightarrow A_x \in M_{ij}$$

BLP abstract operations

Get access: initiate access to object, i.e., add (S_i, O_i, A_x) to **b**

Release access: release access to object, i.e., remove (S_i, O_j, A_x) from **b**

Change object level: change the value of $f(O_j)$ for some object O_j

Change subject level: Change the value of $f(S_i)$ for some subject S_i

Give access permission: grant an access mode, i.e., add $\mathbf{A}_{\mathbf{x}}$ to $\mathbf{M}_{\mathbf{ij}}$

Revoke access permission: delete an access mode, i.e., remove $\mathbf{A}_{\mathbf{x}}$ from $\mathbf{M}_{\mathbf{ij}}$

Create an object: add a new object **O**_j with security level **f**(**O**_j)

Delete an object: remove object **O**_j

Security of abstract operations

Get access: add $(S_{i'}, O_{i'}, read)$ to **b**

$$f(S_i) \ge f(O_j)$$
 and $read \in M_{ij}$

Get access: add (S_i, O_j, write) to b

$$f(S_i) \le f(O_j)$$
 and write $\in M_{ij}$

Change object/current level: change the value of $f(O_j)$ (similarly for $f(S_i)$)

$$\forall i . (S_{i'} O_{j'} read) \in b \Rightarrow f(S_i) \ge f(O_j)$$

 $\forall i . (S_{i'} O_{j'} write) \in b \Rightarrow f(S_i) \le f(O_j)$

Revoke access permission: remove $\mathbf{A}_{\mathbf{x}}$ from $\mathbf{M}_{\mathbf{ij}}$

$$(S_i, O_j, A_x) \in b$$

When action violates the condition

- action is forbidden (error), or
- state should be updated, e.g.,
 release accesses that violate the new permissions or levels (make the state secure)

BLP security proof

Secure state: state (b, M, f) is secure if and only if every element of b satisfies the three properties

State transition: state (b, M, f) is changed by any operation that changes b, M or f

Security Theorem: a system starting from a **secure state** is **secure** iff any operation preserves the three properties (can be formally proved)

It is **theoretically possible** to prove that an actual implementation (or system design) is **secure** by proving that any action that affects the state of the system satisfies the three properties

For a complex system, such a proof can hardly cover all cases

⇒ Still, formal proof can lead to **more** secure design and implementation

Applications of BLP model

Implementing BLP in RBAC (1)

Constraint on users: For each subject **s** a security **clearance L**(**s**) is assigned

Permissions: For each role **r** and object **o**, assign **read/write** permission (access matrix)

Constraint on objects: For each object **o** a security **classification L(o)** is assigned

The read-level of a role **r**, denoted **r-level**(**r**), is the **least upper bound** of the security levels of the objects for which **read** is in the permissions of **r**

The write-level of a role r, denoted w-level(r), is the greatest lower bound of the security levels of the objects for which write is in the permissions of r

Implementing BLP in RBAC (2)

Constraint on role assignment: the clearance of the subject must dominate the r-level of the role and be dominated by the w-level of the role

$$L(S) \ge r\text{-level}(r)$$

$$L(S) \leq w$$
-level (r)

The r-level of the role indicates the **least security classification** that dominates the level of objects readable from the role

Simple security property demands that a subject is assigned to a role only if the subject's clearance is at least as high as the r-level of the role

(dually for **write** access, *-property)

Trusted systems

Trust: confidence that system meets specifications, e.g., through **formal analysis** or **code review**

Trusted computing base (TCB): part of the system **enforcing** a particular policy, small enough to be **analyzed**

Evaluation: assessing if system has the **claimed security properties**

Trusted Platform Module (TPM)

TPM is a **hardware module** that is at the heart of a hardware/software approach to trusted computing

Standardized by the <u>Trusted</u> <u>Computing Group</u>

TPM is **integrated** in the CPU, the motherboard, or in smarcards

It is a hardware, **tamper resistant**Trusted Computing Base (TCB)

The TPM works with **TC-enabled software**, including the OS and applications

The software can be assured that the data it receives are **trustworthy**, and the system can be assured that the software itself is **trustworthy**

Three basic services: authenticated boot, certification, and encryption

Authenticated boot service

Responsible for booting the entire operating system, **assuring** that it is an **approved version** for use

Boot happens in **stages**:

- Boot ROM is loaded
- Boot Block on storage is loaded
- Larger blocks are brought in, until the full OS is loaded

At each stage, the TPM checks that valid software has been brought in, e.g. verifying a digital signature associated with the software

The TPM keeps a **tamper-evident log** of the loading process

⇒ a cryptographic hash function is used to detect any tampering with the log

Authenticated boot service

The tamper-evident log contains a record that establishes exactly, which version of the OS and which of its modules are running

Trust boundary can be expanded to include additional hardware and application and utility software

⇒ approved list of hardware and software components The TC-enabled system checks whether any new component

- is on the approved list
- is digitally signed
- has a serial number that has not been revoked
- ⇒ hardware, system software, and applications in a well-defined state with approved components.

Certification service

A mechanism to certify the (trusted) configuration to **other parties**

The TPM produces a **digital certificate** by **signing** a description of
the configuration information using
the TPM's private key

Other local or remote parties have **confidence** that an unaltered configuration is in use

Notice that:

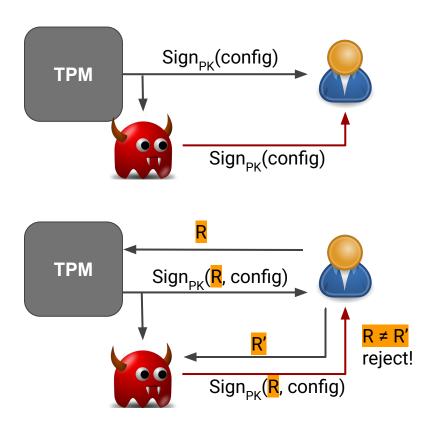
- TPM is trustworthy (no need of a further certification of the TPM)
- Only the TPM possesses this particular **private key**
- TPM's public key can be used to verify the signature
- Hierarchical trust: TPM certifies hardware/OS, OS can certify applications, etc.

Preventing replay attacks

An attacker might

- 1. **intercept** TPM certification
- 2. **compromise** the system
- "replay" the certification when needed to prove trustworthiness of the attacked system

Solution: TPM includes a **random challenge** R from the requester in the signature to prevent "replays"



Encryption

Enables the encryption of data in such a way that the data can be decrypted only by a certain machine, and only if that machine is in a certain (trusted) configuration

Idea: one **master secret key** used to derive **many encryption keys**, one for each trusted configuration

decryption is possible only in the same configuration

Hierarchical trust: provide an encryption key to a (certified) application so that the application can encrypt data

Decryption can only be done by the desired version of the desired application running on the desired version of the desired OS

Even **remote**, if TPMs share master keys

Example: protected storage

File **encrypted** and saved in a local storage

The encryption key is **encrypted by the TPM** using the master key and
stored together with the file

The encrypted key is associated to the specification of hardware / software configuration that is authorized to access the key

Application requests to decrypt the encrypted key:

- TPM verifies that hardware / software configuration matches the required one
- TPM decrypts the key and passes it to the application
- 3. Application decrypts the file and is **trusted to discard the key**